

BitFlow Examples:

- **Multi-Buffer Grab - Circular Acquisition.vi**
The Board is set up in the Circular Acquisition mode. The number of buffers is specified by the user, and acquisition is performed continuously into the allocated buffers. This example file also demonstrates the IO read/write functionality.
- **Multi-Buffer Grab - Sequential Acquisition.vi**
The board is set up in the Sequential Acquisition mode. The number of buffers are specified by the user. An image is acquired into each buffer. Once the acquisition is complete, the user can run through the images acquired.
- **Multi-Multi-Buffer Grab.vi**
Similar to the Circular Acquisition example. But in this case acquisition is done simultaneously from two boards.
- **RegRW.vi**
Example demonstrating the ability to read/write register values using:
 1. Bootstrap registers
 2. Register addresses
 3. SFNC
- **CLSerialCom.vi**
Demonstrates the sending and receiving commands to and from the camera, over the serial communications portion of the Camera Link cable. The VIs used directly control the UART on the BitFlow board.
- **MultiImageBuffer Example.vi**
Example demonstrating support for the “Pixel-Shifting” technology Vieworks VN-200MX-M/C 30 camera. The LED_XXXX bits are used to get the stage mode. These bits are set in the BFML file. The resulting image can be composed of upto 9 images based on the stage-mode setting.
- **ExposureControl.vi**
Example demonstrating use of the timing generator on the board for exposure control. The “BF Exposure Set” and “BF Exposure Set” VIs are used to program the board’s timing generator, used to create waveforms to control the line/frame rate and exposure time of cameras.